

**City University of Hong Kong**  
**Course Syllabus**

**offered by Department of Materials Science and Engineering**  
**with effect from Semester A 2018 /19**

---

---

**Part I Course Overview**

**Course Title:** **Electronic Packaging and Materials**

---

**Course Code:** **MSE4171**

---

**Course Duration:** **One semester**

---

**Credit Units:** **3**

---

**Level:** **B4**

---

**Proposed Area:**  
*(for GE courses only)*

- Arts and Humanities  
 Study of Societies, Social and Business Organisations  
 Science and Technology
- 

**Medium of Instruction:**

**English**

---

**Medium of Assessment:**

**English**

---

**Prerequisites:**  
*(Course Code and Title)*

**Nil**

---

**Precursors:**  
*(Course Code and Title)*

**AP2102/MSE2102 Introduction to Materials Engineering**

---

**Equivalent Courses:**  
*(Course Code and Title)*

**AP4171 Electronic Packaging and Materials**

---

**Exclusive Courses:**  
*(Course Code and Title)*

**Nil**

---

## Part II Course Details

### 1. Abstract

This course provides students with the basic understanding of electronic packaging. It also introduces the important issues relating to materials behaviour in electronic packaging.

### 2. Course Intended Learning Outcomes (CILOs)

(CILOs state what the student is expected to be able to do at the end of the course according to a given standard of performance.)

No.	CILOs <sup>#</sup>	Weighting* (if applicable)	Discovery-enriched curriculum related learning outcomes (please tick where appropriate)		
			A1	A2	A3
1.	Recognize the basic principles of packaging architectures, package designs, reliability and failure analysis.		✓		
2.	Identify the common failure modes and reliability issues.			✓	
3.	Demonstrate the choice of common packaging materials.			✓	
4.	Demonstrate the effect of operation parameters on the behaviour of common packaging materials during their life cycles.			✓	
5.	Apply appropriate reliability testing models for electronic packages.				✓

\* If weighting is assigned to CILOs, they should add up to 100%.

<sup>#</sup> Please specify the alignment of CILOs to the Gateway Education Programme Intended Learning outcomes (PILOs) in Section A of Annex.

A1: Attitude

Develop an attitude of discovery/innovation/creativity, as demonstrated by students possessing a strong sense of curiosity, asking questions actively, challenging assumptions or engaging in inquiry together with teachers.

A2: Ability

Develop the ability/skill needed to discover/innovate/create, as demonstrated by students possessing critical thinking skills to assess ideas, acquiring research skills, synthesizing knowledge across disciplines or applying academic knowledge to self-life problems.

A3: Accomplishments

Demonstrate accomplishment of discovery/innovation/creativity through producing /constructing creative works/new artefacts, effective solutions to real-life problems or new processes.

### 3. Teaching and Learning Activities (TLAs)

(TLAs designed to facilitate students' achievement of the CILOs.)

TLA	Brief Description	CILO No.					Hours/week (if applicable)
		1	2	3	4	5	
Lecture	To cover basic concepts of electronic packaging, materials and reliability.	✓	✓				2 hours/week
Discussion (internet)	Through technical communication, reinforce the learning of various topics of electronic packaging, materials and reliability.	✓	✓	✓	✓	✓	1 hour/week for 5 weeks
Oral presentation	Present the ideas relating to the discussion.	✓	✓	✓	✓	✓	1 hour/week for 5 weeks

**4. Assessment Tasks/Activities (ATs)**

*(ATs are designed to assess how well the students achieve the CILOs.)*

Assessment Tasks/Activities	CILO No.						Weighting*	Remarks
	1	2	3	4	5			
Continuous Assessment: 50 %								
Discussion (internet)	✓	✓	✓	✓	✓		25%	
Oral presentation	✓	✓	✓	✓	✓		25%	
Examination^: 50% (duration: 1.5 hours)								
<i>* The weightings should add up to 100%.</i>							100%	

^ For a student to pass the course, at least 30% of the maximum mark for the examination must be obtained

## 5. Assessment Rubrics

*(Grading of student achievements is based on student performance in assessment tasks/activities with the following rubrics.)*

Assessment Task	Criterion	Excellent (A+, A, A-)	Good (B+, B, B-)	Fair (C+, C, C-)	Marginal (D)	Failure (F)
1. Discussion (internet)	CAPACITY for SELF-DIRECTED LEARNING to research on electronic packaging, materials and reliability	High	Significant	Moderate	Basic	Not even reaching marginal levels
2. Oral presentation	ABILITY to EXPLAIN in DETAIL and with ACCURACY aspects of electronic packaging	High	Significant	Moderate	Basic	Not even reaching marginal levels
3. Examination	ABILITY to EXPLAIN the technical details of electronic packaging	High	Significant	Moderate	Basic	Not even reaching marginal levels

**Part III Other Information** (more details can be provided separately in the teaching plan)

**1. Keyword Syllabus**

- Introduction to electronic packaging (10 hours)  
Semiconductors and microelectronic devices. Wafer level packaging. Integrated circuits, packaging and assembly. Optoelectronic packaging. Sealing and encapsulation. Board assembly. Packaging and electronic products.
- Packaging materials and processes (8 hours)  
The role of materials in electronic packaging. Packaging materials and properties. Material processes. Future trends.
- Package reliability (6 hours)  
Thermal and mechanical reliability. Failure modes and mechanisms. Reliability qualifications. Failure analysis. Experimental methods and tools for reliability analysis.

**2. Reading List**

**2.1 Compulsory Readings**

*(Compulsory readings can include books, book chapters, or journal/magazine articles. There are also collections of e-books, e-journals available from the CityU Library.)*

Nil

**2.2 Additional Readings**

*(Additional references for students to learn to expand their knowledge about the subject.)*

1.	Rao R Tummala, "Fundamentals of Microsystems Packaging", McGrawHill, New York, 2001. (CityU Lib Cat No TK7870.15 .F86 2001)
2.	Michael G Pecht et al, "Electronic packaging materials and their properties", Boca Raton: CRC Press, c1999. (CityU Lib Cat No TK7870.15 .E4222 1999)
3.	Deborah D L Chung, editor, "Materials for electronic packaging", Boston: Butterworth-Heinemann c1995. (CityU Lib Cat No TK7870.15 .M38 1995)
4.	Journals: IEEE Transactions on Advanced Packaging IEEE Transactions on Components and Packaging Technologies IEEE Transactions on Electronics Packaging Manufacturing Journal of Electronic Materials Journal of Materials Research ASME J Electronic Packaging